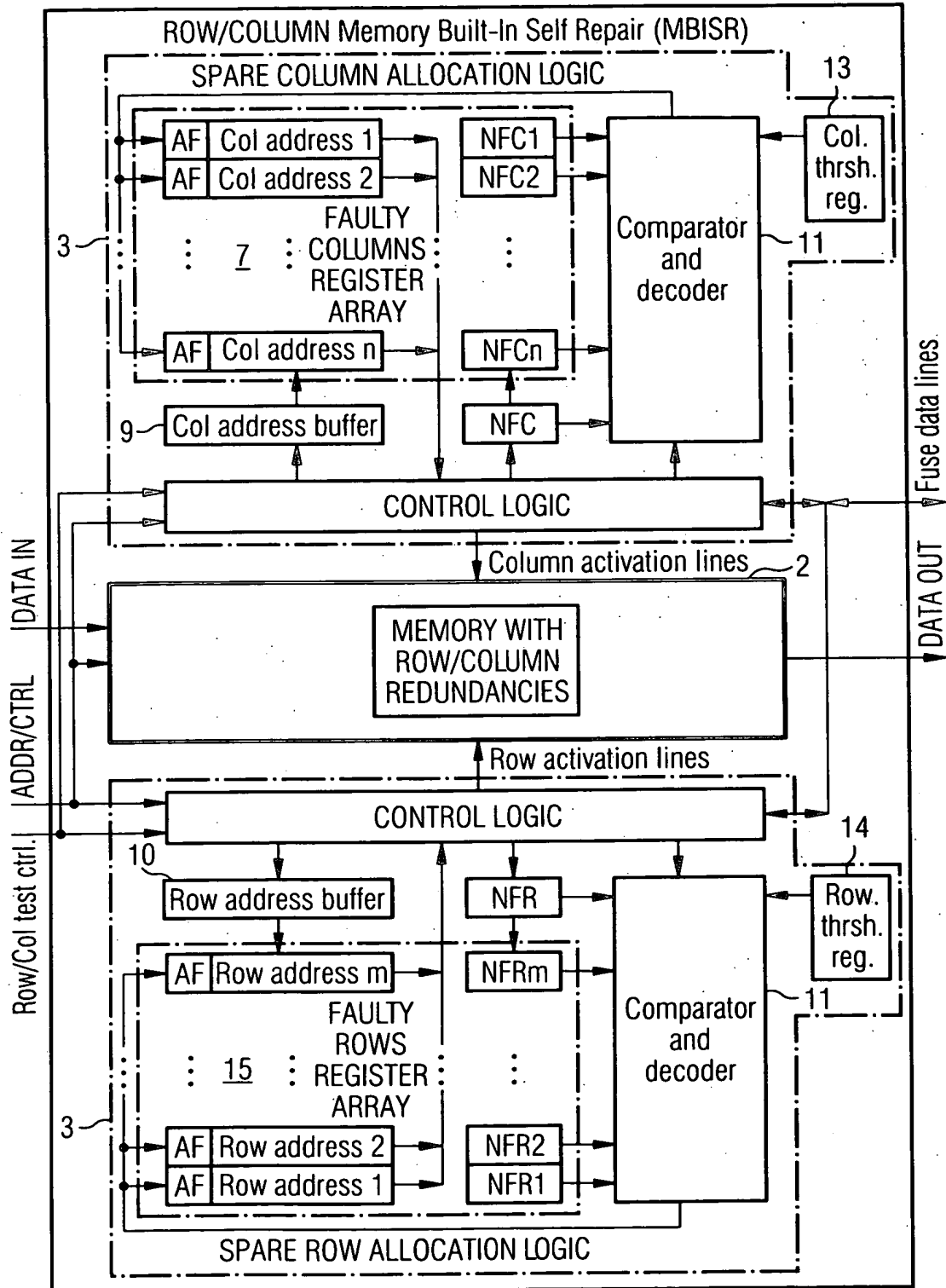


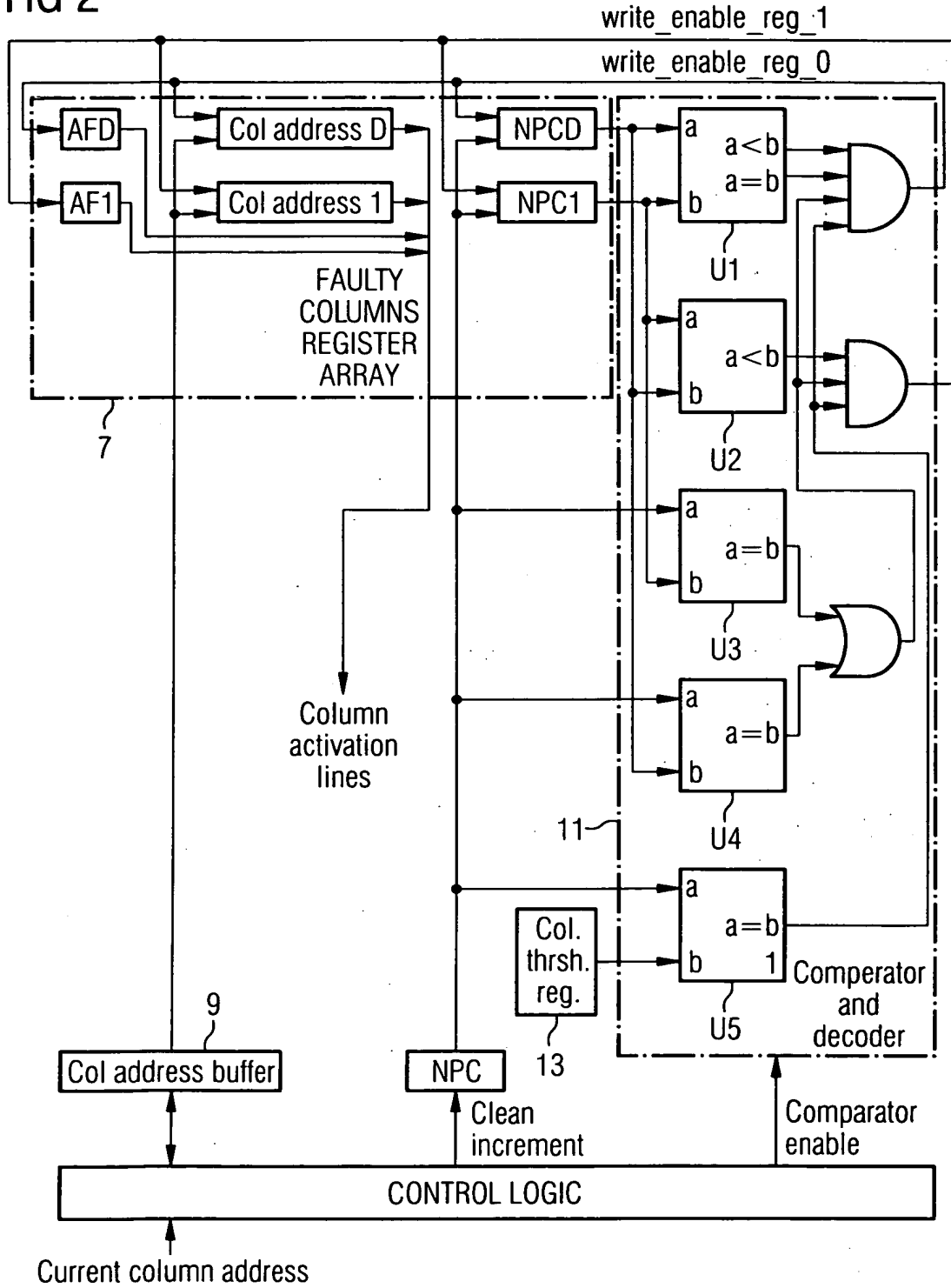
1/8

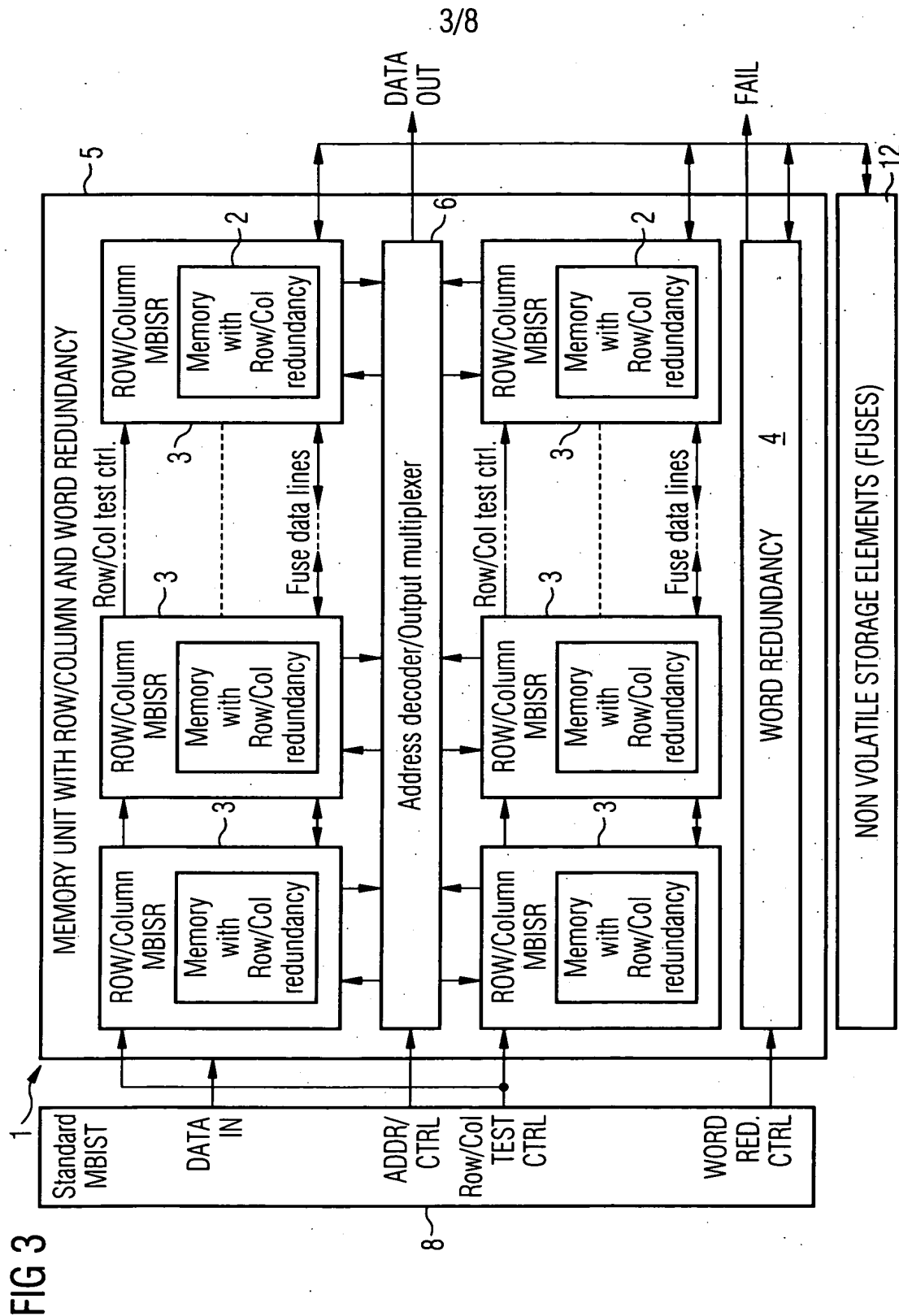
FIG 1



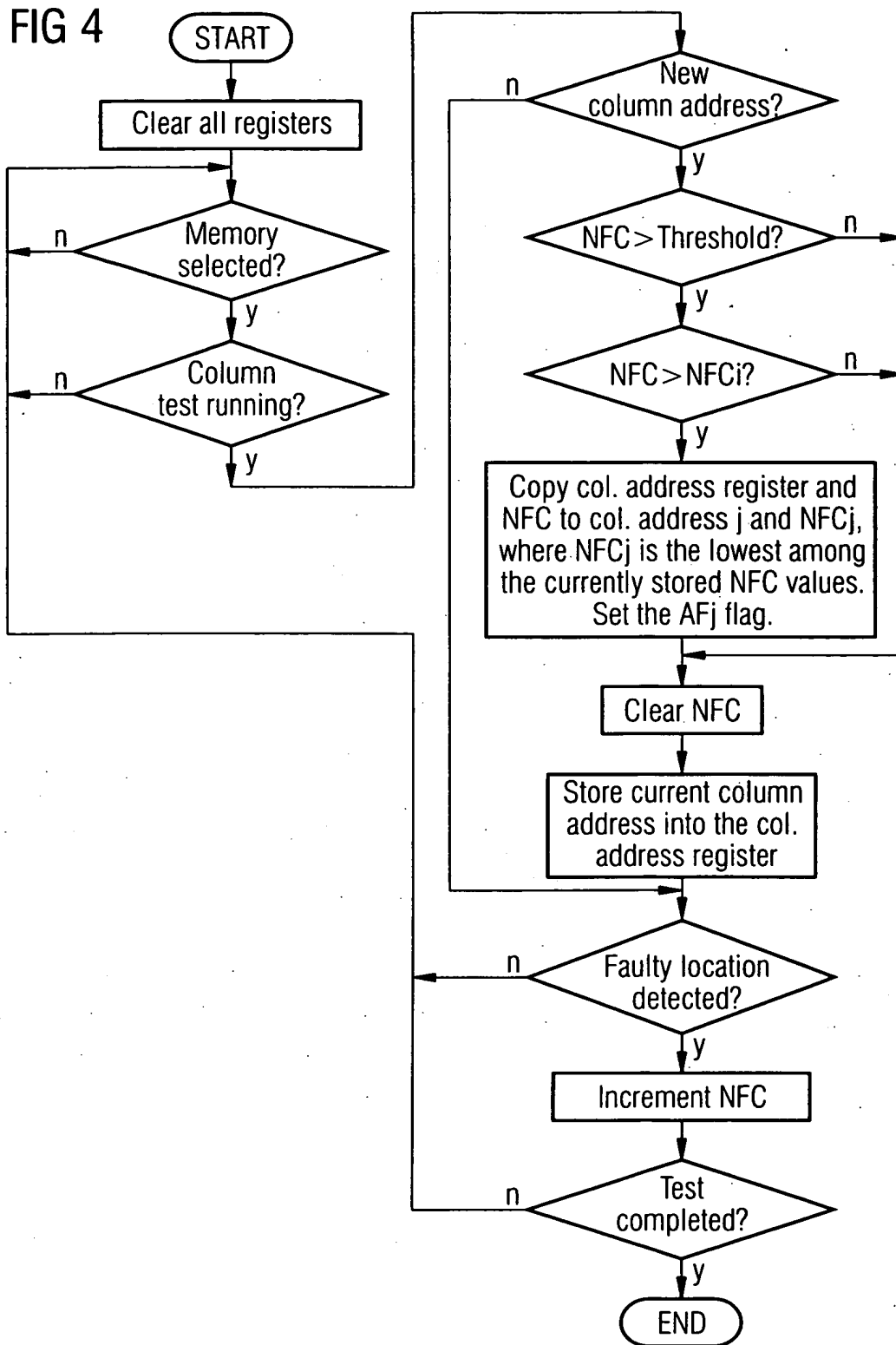
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FIG 2

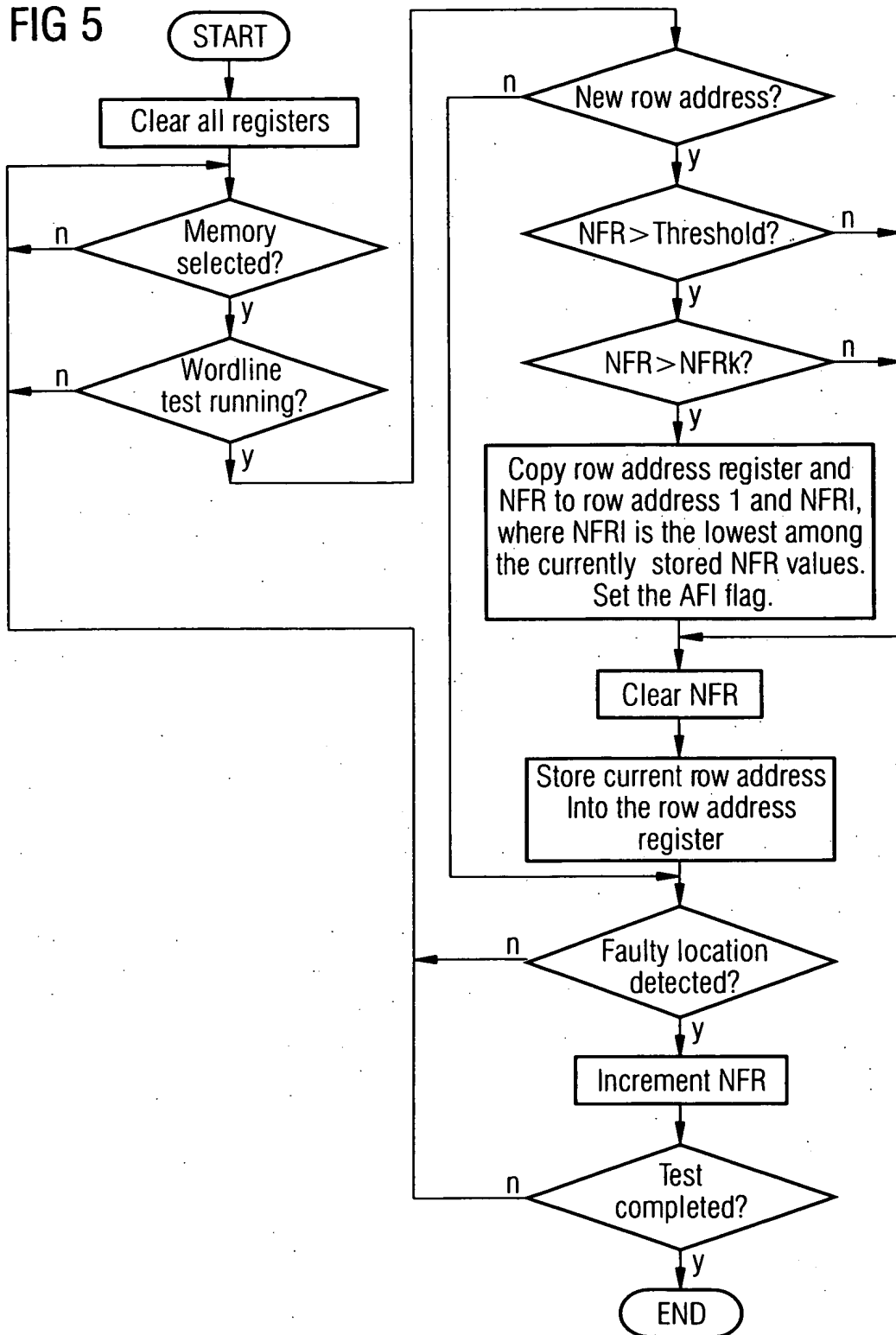




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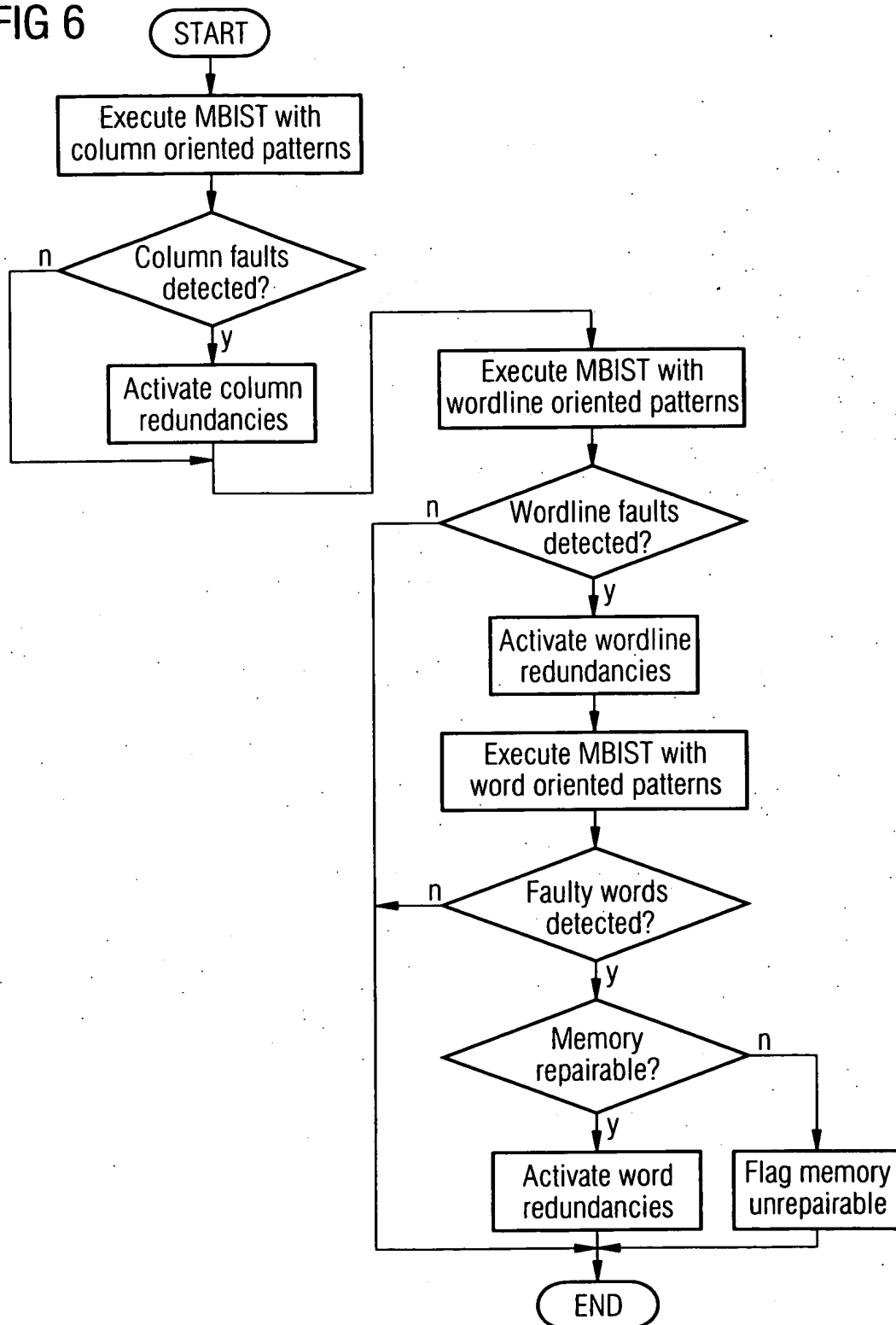


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FIG 6



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FIG 7

MEMORY ARRAY

	W0				W1				W2				W3				W4				W5				W6			
	C0	C1	C2	C3	C4	C5	C6	C7	C8	C9	C10	C11	C12	C13	C14	C15	C16	C17	C18	C19	C20	C21	C22	C23	C24	C25	C26	C27
R0																												
R1																												
R2																												
R3																												
R4																												
R5																												
R6																												
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R33																												
R35																												
R35																												

□ = Fault-free cell
 ▨ = Faulty cell

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FIG 8

